

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Andrew Pickering, et al. Docket: TI-31299
Serial No.: TBD Examiner: TBD
Filed: Herewith Art Unit: TBD
For: IMPROVEMENTS IN OR RELATING TO SYSTEMS FOR DATA
TRANSMISSION

PRELIMINARY AMENDMENT

Assistant Commissioner For Patents
Washington, D. C. 20231

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<u>Karen Vertz</u> Karen Vertz	<u>7-25-01</u> Date

Sir:

Before examination of the above-identified patent application, please make the following amendments:

IN THE SPECIFICATION:

After the title, insert --This application claims priority under 35 USC §(e)(1) of British Application Number 0028134.5 filed November 17, 2000.--

IN THE CLAIMS:

15. (Amended) An encoder as claimed in claim 9 comprising first and second sets of switches, one switch from each of the first and second sets being connected to a respective one of the terminals, the encoder being arranged to activate a selected one of the first set of switches in order to provide one of the active signals on the terminal to which that switch is connected and the encoder arranged to activate a selected one of the second set of switches in order to provide the other active signal on a terminal to which that switch is connected.

19. (Amended) An encoder as claimed in claim 15 wherein each terminal of the encoder is coupled, via a resistor, to a common node.

25. (Amended) A decoder as claimed in claim 24 comprising:

- a first receiver sub-circuit;
- a second receiver sub-circuit;
- a data decoder;
- a data output coupled to the data decoder,

wherein:

the first receiver sub-circuit has a respective switching element for each of the said terminals controlled by the signal on that terminal providing a respective output, each output of the first receiver sub-circuit indicating when the terminal controlling the switching element is at a first voltage level;

the second receiver sub-circuit has a respective switching element for each of the said terminals controlled by the signal on that terminal providing a respective output, each output of the second receiver sub-circuit indicating when the terminal controlling the switching element is at a second voltage level; and

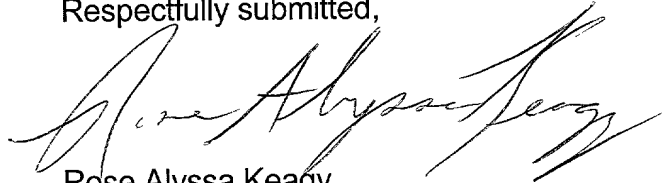
the output of the first and second receiver sub-circuits are coupled to inputs of the data decoder, the data decoder is arranged to determine the data symbol transmitted in

response to which outputs of the first and second receiver sub-circuits indicate the presence of the first and second voltage levels and to indicate that data symbol at the data output.

28. (Amended) A system comprising an encoder as claimed in claim 8 and a decoder as claimed in claim 21.

29. (Amended) A method of transmitting data comprising encoding it as a series of symbols using the signal of claim 1.

Respectfully submitted,



Rose Alyssa Keagy
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VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE SPECIFICATION:

After the title, insert --This application claims priority under 35 USC §(e)(1) of British Application Number 0028134.5 filed November 17, 2000.--

IN THE CLAIMS:

15. (Amended) An encoder as claimed in [any one of claims 9 to 14] claim 9 comprising first and second sets of switches, one switch from each of the first and second sets being connected to a respective one of the terminals, the encoder being arranged to activate a selected one of the first set of switches in order to provide one of the active signals on the terminal to which that switch is connected and the encoder arranged to activate a selected one of the second set of switches in order to provide the other active signal on a terminal to which that switch is connected.

19. (Amended) An encoder as claimed in [any one of claims 15 to 18] claim 15 wherein each terminal of the encoder is coupled, via a resistor, to a common node.

25. (Amended) A decoder as claimed in claim 24 comprising:

- a first receiver sub-circuit;
- a second receiver sub-circuit;
- a data decoder;
- a data output coupled to the data decoder,

wherein:

the first receiver sub-circuit has a respective switching element for each of the said terminals controlled by the signal on that terminal providing a respective output, each output

of the first receiver sub-circuit indicating when the terminal controlling the switching element is at a first voltage level;

the second receiver sub-circuit has a respective switching element for each of the said terminals controlled by the signal on that terminal providing a respective output, each output of the second receiver sub-circuit indicating when the terminal controlling the switching element is at a second voltage level; and

the output of the first and second receiver sub-circuits are coupled to inputs of the data decoder, the data decoder is arranged to determine the data symbol transmitted in response to which outputs of the first and second receiver sub-circuits indicate the presence of the first and second voltage levels and to indicate that data symbol at the data output.

28. (Amended) A system comprising an encoder as claimed in [any one of claims 8 to 20] claim 8 and a decoder as claimed in [any one of claims 21 to 27 as appropriate] claim 21.

29. (Amended) A method of transmitting data comprising encoding it as a series of symbols using the signal [claimed in any one of claims 1 to 7] of claim 1.

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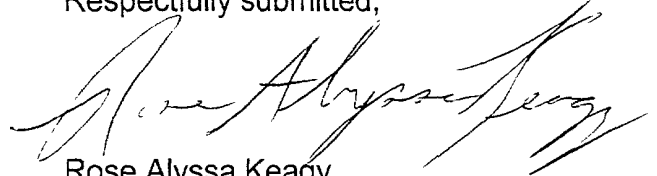
the output of the first and second receiver sub-circuits are coupled to inputs of the data decoder, the data decoder is arranged to determine the data symbol transmitted in

response to which outputs of the first and second receiver sub-circuits indicate the presence of the first and second voltage levels and to indicate that data symbol at the data output.

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the output of the first and second receiver sub-circuits are coupled to inputs of the data decoder, the data decoder is arranged to determine the data symbol transmitted in response to which outputs of the first and second receiver sub-circuits indicate the presence of the first and second voltage levels and to indicate that data symbol at the data output.

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